

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of :

Brad Besmer  
Guy Kendall  
Brian Day

Serial No.: 09/834,013

Group Art Unit : 2181

Filed : April 12, 2001

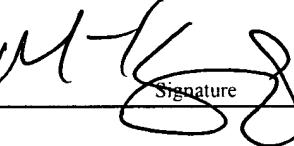
Examiner : Cleary, Thomas J.

For : Method or Asynchronous  
Transaction Queing and Linking

Atty Docket : / 00-437/RCE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

4/26/2005   
Date Signature

## **SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85**

### **Official Draftsman**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Date: 4/26/05

Respectfully submitted,



Sandeep Jaggi

Reg. No. 43,331